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multilevel NOR and NAND flash memories F. Sun, S. Devarajan, K. Rose and T. Zhang Abstract: The design of on-chip error correction systems for multilevel code-storage NOR flash and data-storage NAND flash memories is concerned. The concept of trellis coded modulation (TCM) has been used to design on-chip error correction system for NOR flash. This is motivated by the non-trivial modulation process in m...

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